

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A system comprising:

a process comprising multiple-mapped memory;

a first set of memory mapped onto the multiple-mapped memory;

a second set of memory mapped onto the multiple-mapped memory; and

an address overload circuit to selectively map the multiple-mapped memory to the first set of memory or to the second set of memory, the address overload circuit comprising an address multiplexer[[,]] to receive a first address and a second address from an address translator coupled to the address multiplexer, and a data multiplexer coupled to the first set of memory and the second set of memory to output information from the first set of memory or the second set of memory.

Claim 2 (previously presented): A system as defined in Claim 1, wherein the second set of memory comprises instructions to execute a protected function.

Claim 3 (previously presented): A system as defined in Claim 1, further comprising a transfer agent to receive parameters from the process and to assume control of execution of the process when the multiple-mapped memory is mapped to a protected set of memory.

Claim 4 (previously presented): A system as defined in Claim 3, wherein the transfer agent is to call a protected function.

Claim 5 (previously presented): A system as defined in Claim 4, wherein the transfer agent is to call the protected function using parameters received from the process.

Claim 6 (original): A system as defined in Claim 4, wherein the transfer agent is stored on nonvolatile memory.

Claim 7 (previously presented): A system as defined in Claim 6, wherein the transfer agent is to execute on internal memory.

Claim 8 (canceled)

Claim 9 (previously presented): A method comprising:

executing a process that comprises multiple-mapped memory;

determining whether the process is a trusted process;

if the process is determined not to be a trusted process, mapping the multiple-mapped memory to unprotected memory; and

if the process is determined to be a trusted process, mapping the multiple-mapped memory to protected memory, copying a transfer agent to a second memory, transferring parameters from the process to the transfer agent, and controlling execution of the process with the transfer agent.

Claim 10 (canceled)

Claim 11 (previously presented): A method as defined in Claim 9, wherein the transfer agent is stored in a first memory.

Claim 12 (canceled)

Claim 13 (previously presented): A method as defined in Claim 9, further comprising:

executing the transfer agent so as to identify a protected function and to call the protected function.

Claim 14 (original): A method as defined in Claim 13, further comprising:
executing the protected function.

Claim 15 (previously presented): A method as defined in Claim 9, further comprising:

operating a trust co-processor to determine whether the process is a trusted process.

Claim 16 (original): A method as defined in Claim 15, further comprising:

executing the transfer agent so as to identify a protected function and to call the protected function.

Claim 17 (original): A method as defined in Claim 16, further comprising:

executing the protected function.

Claim 18 (previously presented): An article comprising a machine-readable storage medium on which there are stored instructions that, if executed, enable a system to:

determine whether a process is a trusted process;

if the process is a trusted process, transfer, at least temporarily, control of the process to a transfer agent and transfer process parameters to the transfer agent;

identify and execute a protected function; and

copy the transfer agent from nonvolatile memory to volatile memory in the course of executing multiple-mapped memory.

Claims 19-21 (canceled)

Claim 22 (previously presented): An article as defined in Claim 18, wherein the instructions, if executed, enable the system to determine whether the process is a trusted process in response to the detection of the multiple-mapped memory.

Claim 23 (canceled)

Claim 24 (previously presented): An article as defined in Claim 18, wherein the instructions, if executed, enable the system to:

by operation of the transfer agent, identify, call, and execute the trusted process.

Claim 25 (canceled)

Claim 26 (currently amended): A system comprising:

an integrated circuit device comprising a processor, internal random access memory (RAM), and internal read only memory (ROM);

unprotected memory;

protected memory;

a process to execute on the internal RAM, the process comprising multiple-mapped memory, the multiple-mapped memory to be mapped to either the protected memory or the unprotected memory;

a trust co-processor to determine whether the multiple-mapped memory is to be mapped to the unprotected memory or to the protected memory;

a circuit coupled to the trust co-processor to map the multiple-mapped memory to the protected memory, the circuit comprising an address multiplexer, an address translator coupled to the address multiplexer, and a data multiplexer, wherein the address multiplexer and the data multiplexer are to be controlled based on an output of the trust co-processor;

a wireless interface coupled to the processor; and

an antenna coupled to the wireless interface.

Claims 27 – 28 (canceled)

Claim 29 (original): A system as defined in Claim 26, further comprising a transfer agent to receive parameters from a trusted process, call a protected function using the parameters, and cause the protected function to execute.

Claim 30 (canceled)

Claim 31 (previously presented): The system of Claim 26, further comprising a memory controller including the circuit.

Claim 32 (currently amended): An apparatus comprising:

an address selector to receive an address and a translated address, the address selector to provide an output mapped to a respective one of a protected storage and an unprotected storage, the address selector to map the output to the protected storage if a process is trusted;

a data selector to receive data from the protected storage and the unprotected storage; and

a trust coprocessor to determine if the process is trusted, wherein the address selector and the data selector are to be controlled by a control signal from the trust coprocessor.

Claim 33 (previously presented): The apparatus of Claim 32, further comprising an address translator coupled to the address selector to generate the translated address.

Claims 34 – 36 (cancel)

Claim 37 (new): The apparatus of Claim 33, wherein the address translator is coupled to receive the address.

Claim 38 (new): The apparatus of Claim 32, wherein the apparatus comprises a memory controller coupled between a processor and the protected storage.